

2823

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: Guo-Qiang Lo
Assignee: Integrated Device Technology, Inc.
Serial No.: 09/838,084 Group No.: 2823
Filed: 04/18/2001 Examiner: Suk S. Foong
For: "DIELECTRIC ANTI-REFLECTIVE COATING SURFACE
TREATMENT TO PREVENT DEFECT GENERATION IN
ASSOCIATED WET CLEAN"

#9
Electron
main

Commissioner of Patents and Trademarks
Box NON-FEE AMENDMENT
Washington, D.C. 20231

Date: September 20, 2002
BEST AVAILABLE COPY 10/8/02

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the outstanding Office Action dated August 27, 2002, in which the Examiner imposed a restriction requirement to election of species for the above-referenced application, Applicant elects to prosecute **Species 1** drawn to including densifying the patterned silicon oxynitride layer through rapid thermal annealing in the presence of oxygen, without traverse. The claims that are readable thereon are Claims 1-4 and 11-19 wherein Claims 1, 2 and 11-19 are considered generic.

Applicant reserves the right to file divisional applications on the non-elected claims.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS MAIL in an envelope addressed to: Box Non-Fee Amendment, Assistant Commissioner for Patents, Washington, D.C., 20231-0001, on September 20, 2002.

Date: 9-20-02

Signature:

Camie Reddick

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